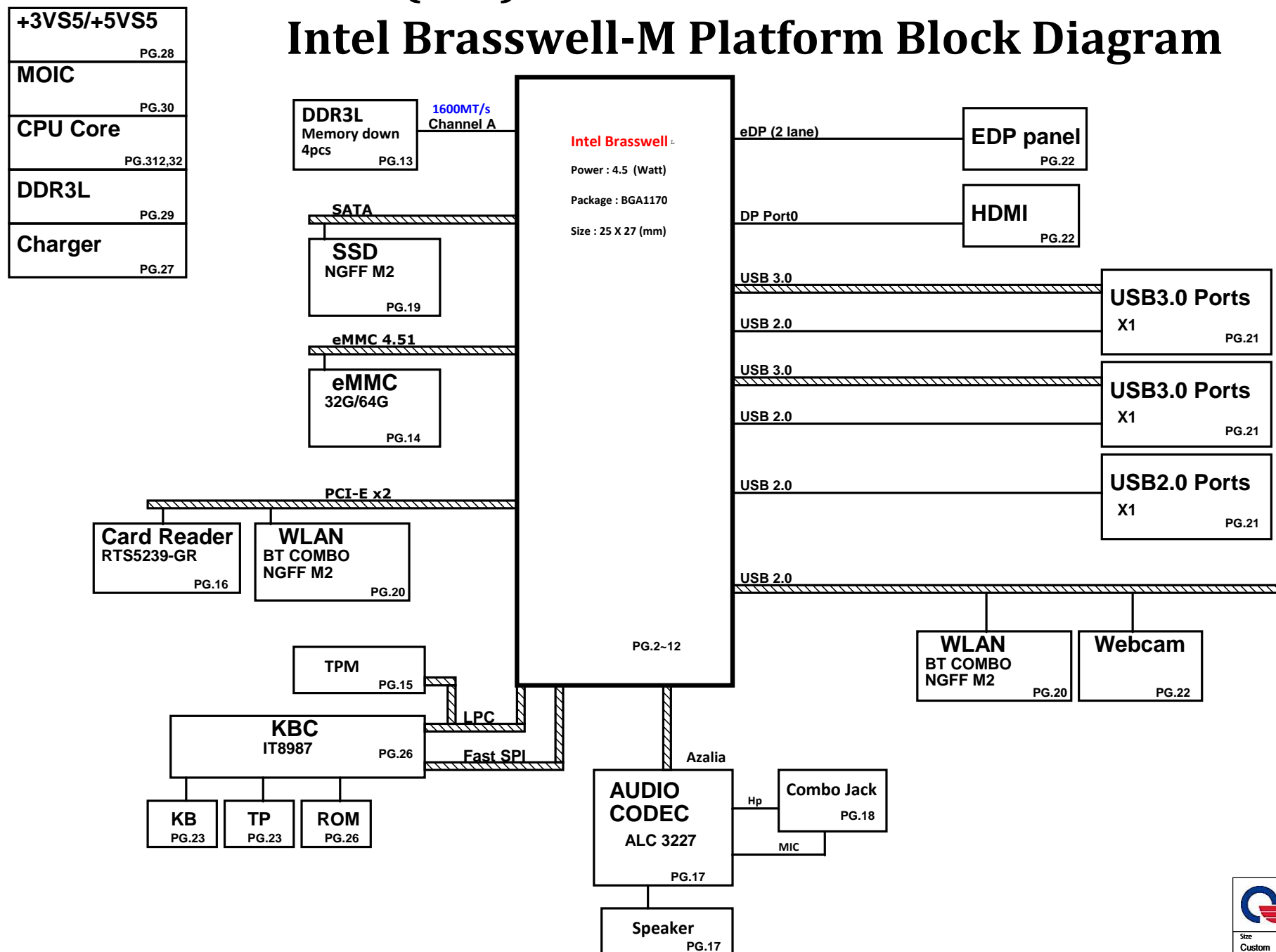
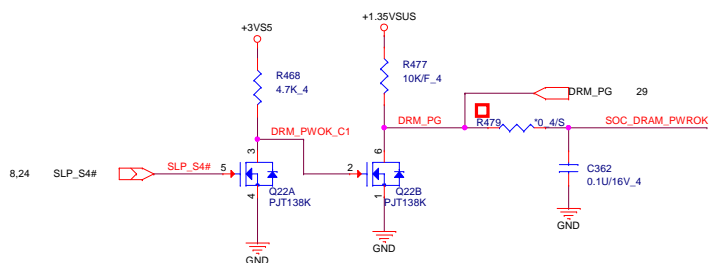
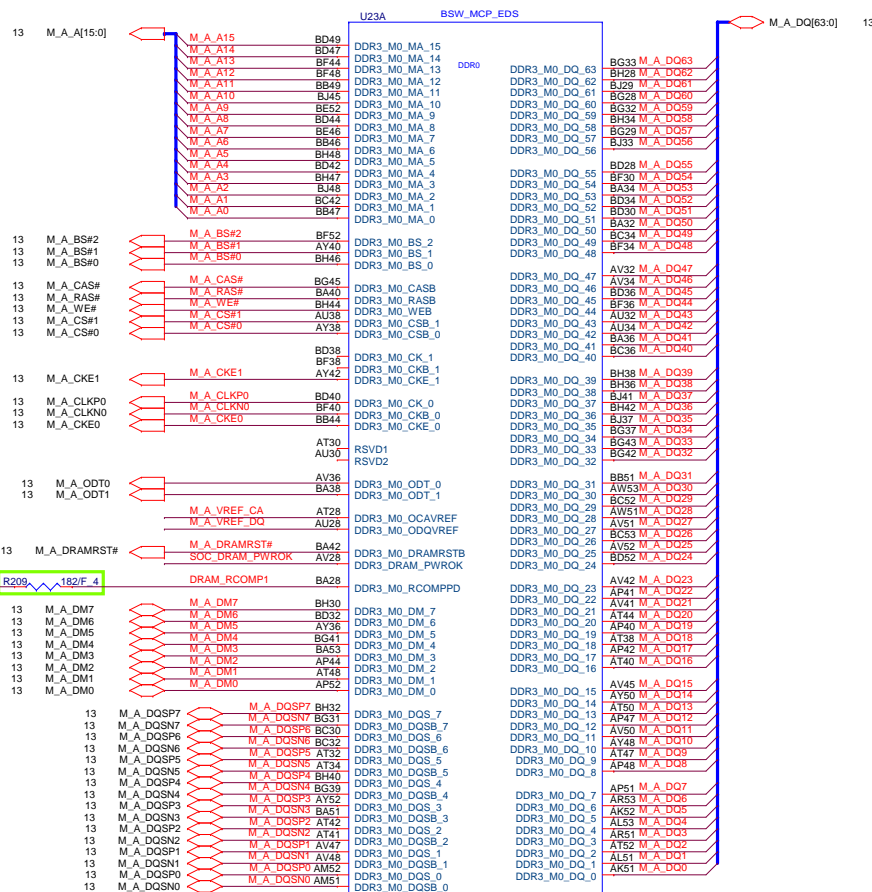
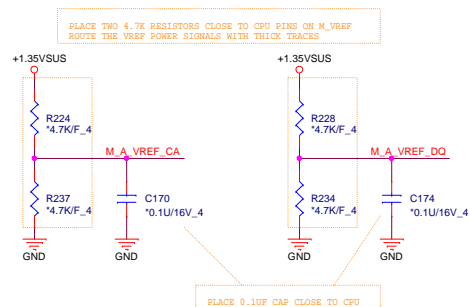
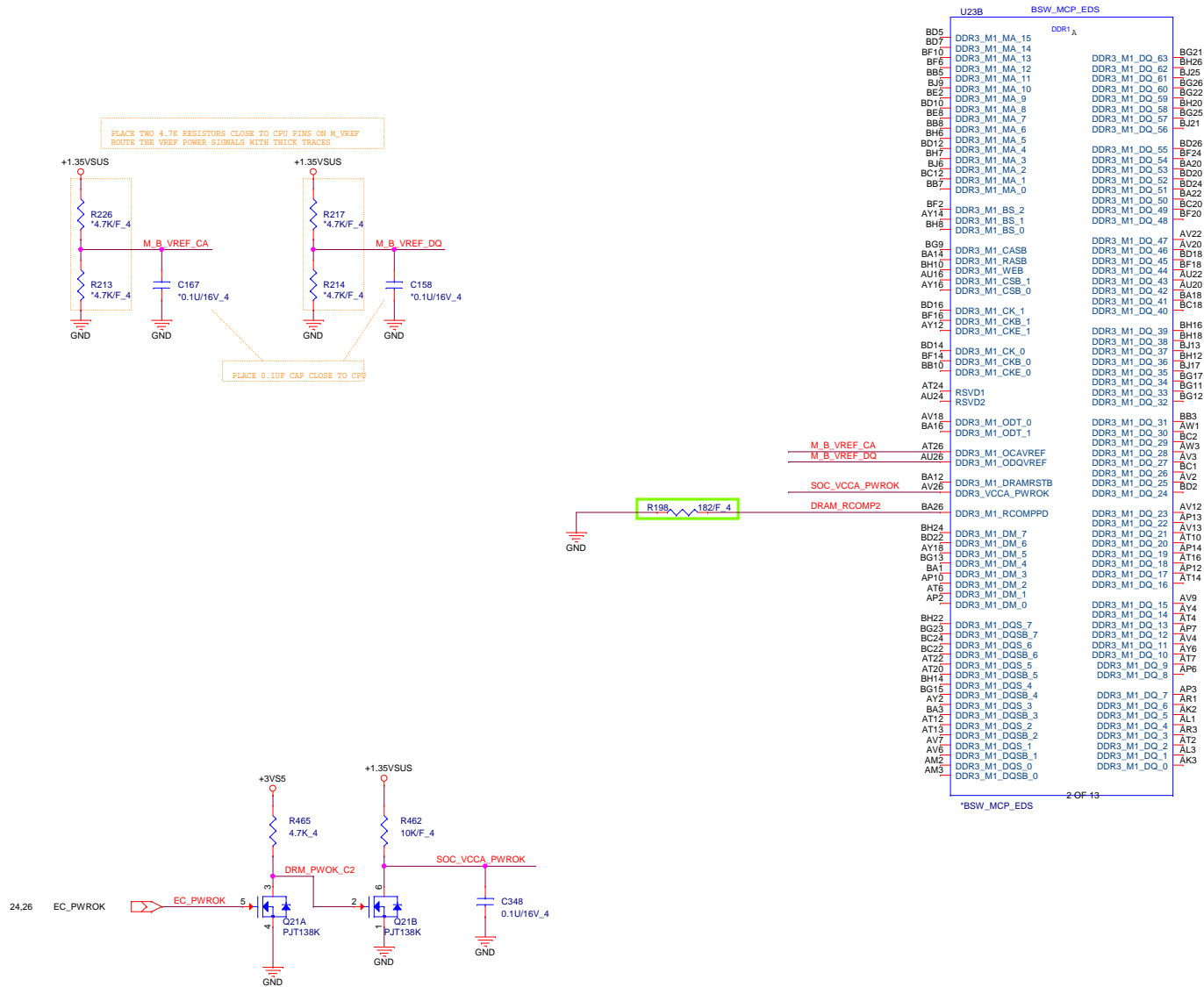


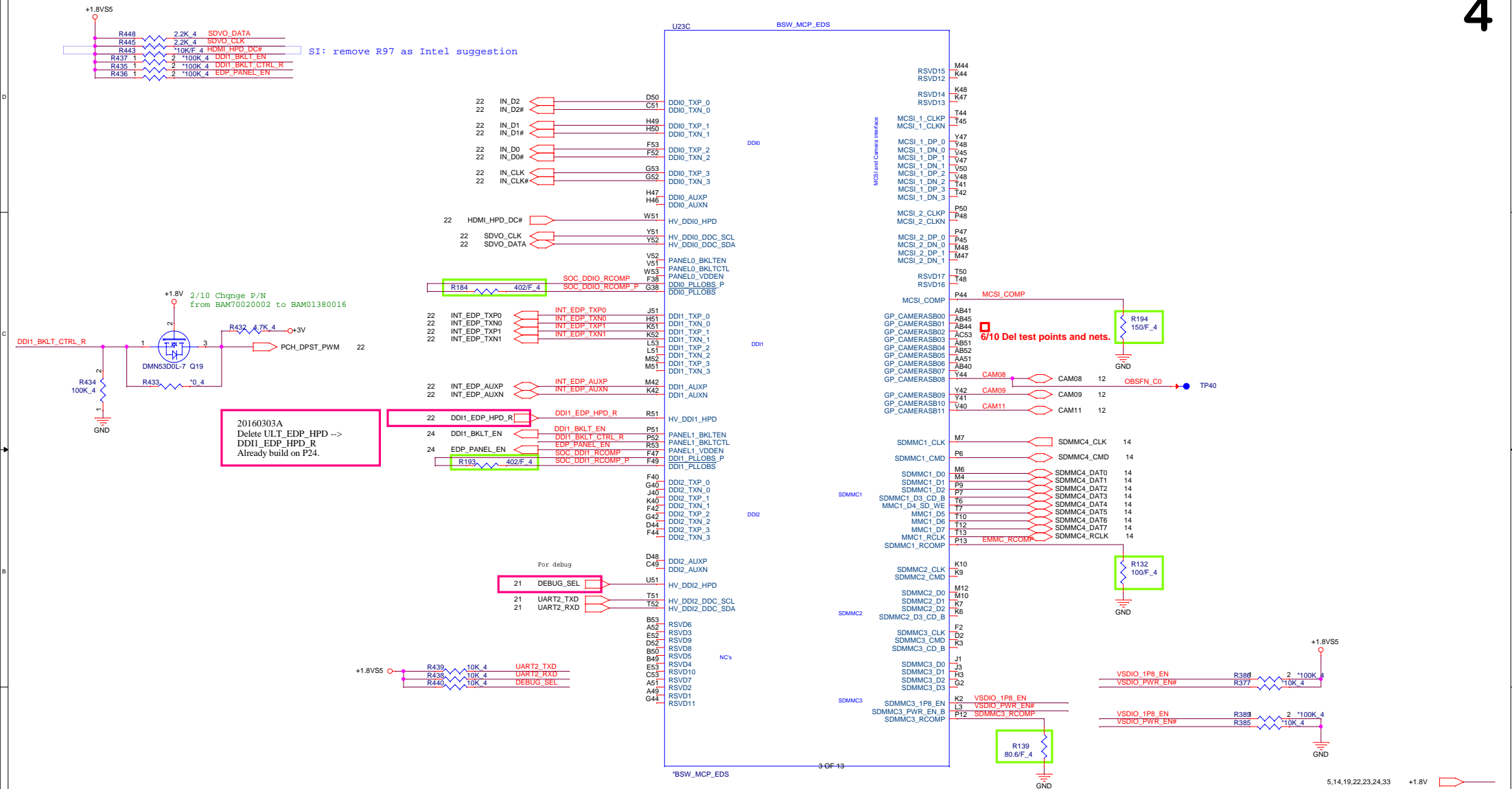
OP9 (14")

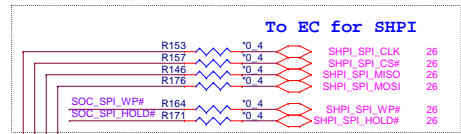
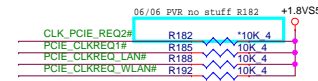
Intel Brasswell-M Platform Block Diagram

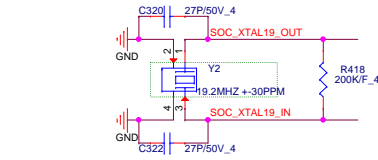




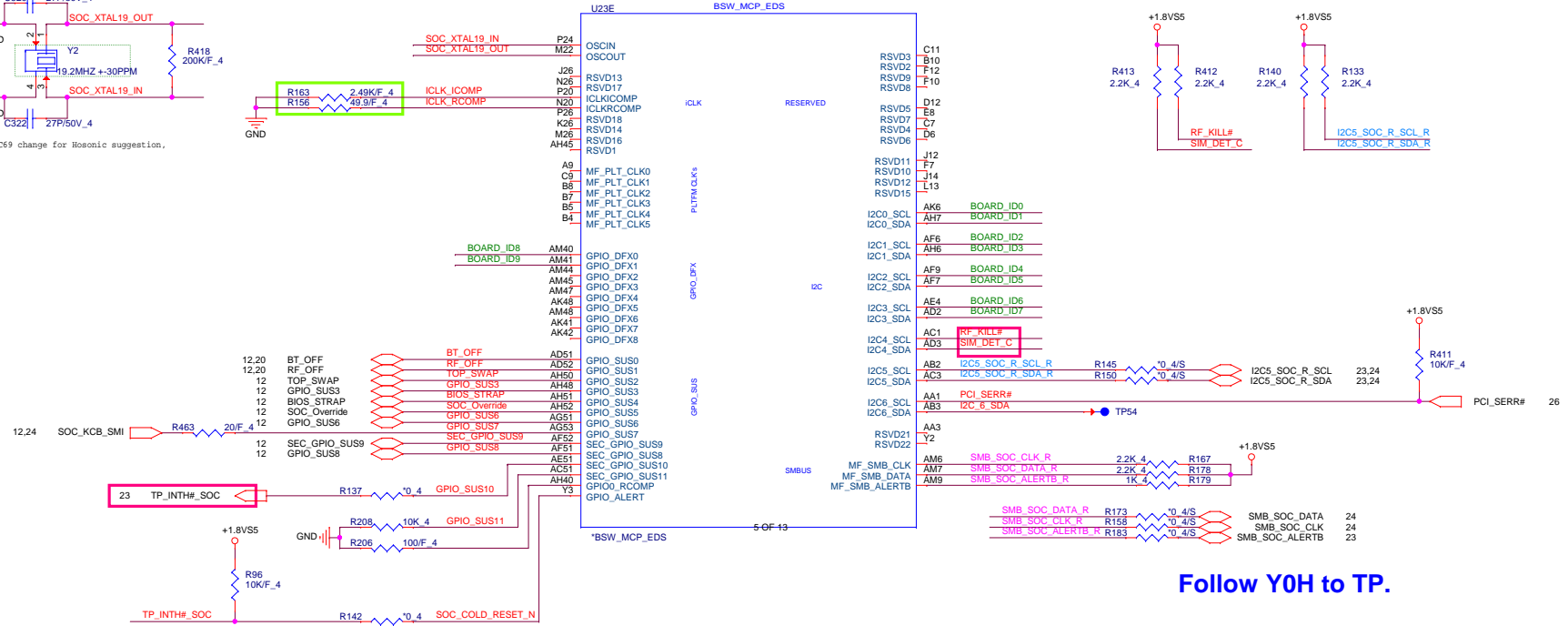








20140109:Si build C68,C69 change for Hosonic suggestion,

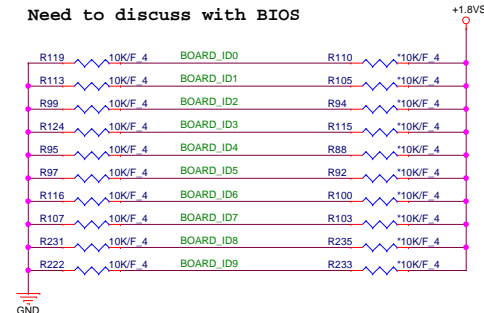


5/4 update Board ID table

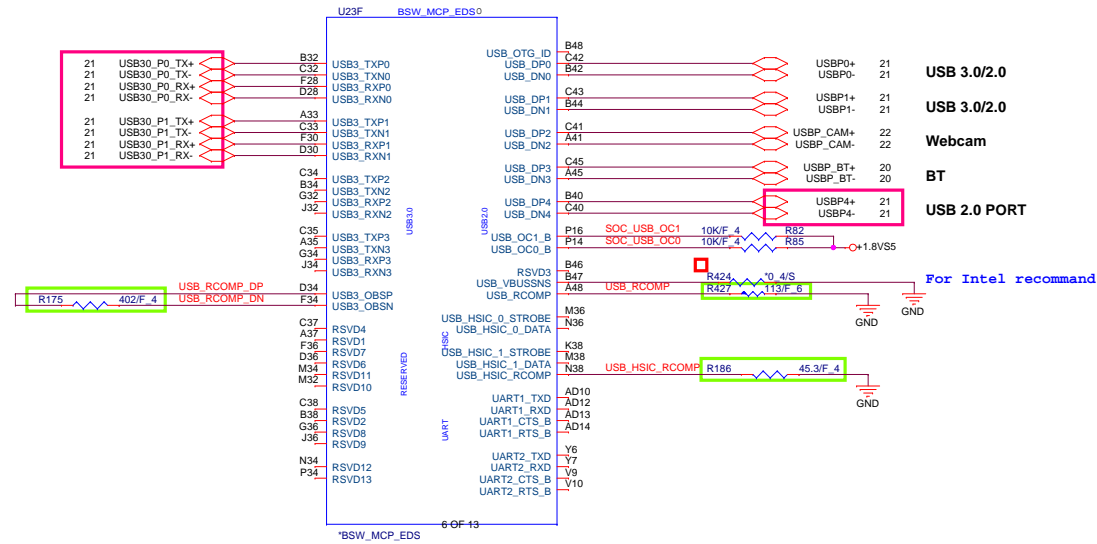
Board ID	BIOS Strap Description	BOARD_ID7	BOARD_ID6	BOARD_ID5	Vender	Quanta PN	Description
BOARD_ID0	Reserve	0	0	0	Hymix-2GB	AKD5PGSTW14	IC SDRAM (96P) H5TC4G63CFR-P8A TOPBSQ
BOARD_ID1	Reserve	0	0	1	Micron-2GB		
BOARD_ID2	1 = SSD 0 = EMMC	0	1	0	Samsung-2GB Q Dye	AKD5JG0T503	IC SDRAM(96P)K4B4G1646E-BYK0(FBGA)TOPBSQ
BOARD_ID8	1 = SO-DIMM 0 = Memory Down	1	0	0			
BOARD_ID9	1 = touch panel 0 = none touch panel	1	0	1	Micron-2GB P Dye	AKD59GSTL04	IC SDRAM(96P) MT41K256M16TW-107:P TOPBSQ
		1	1	0			
		1	1	1			

RAM ID	BIOS Strap Description
BOARD_ID3	1 = TPM 0 = none TPM
BOARD_ID4	1 = RAM SIZE: 4G 0 = RAM SIZE: 2G

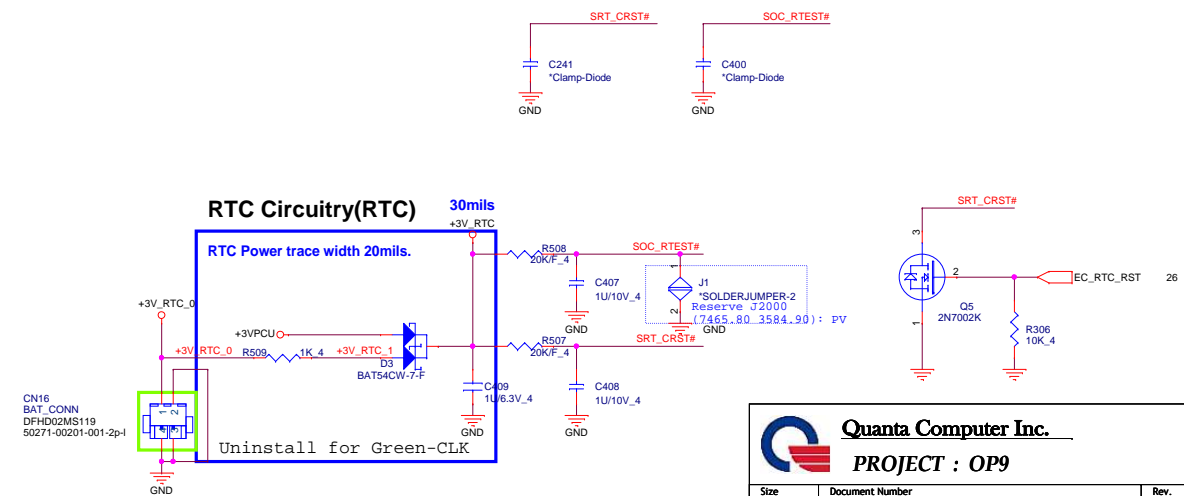
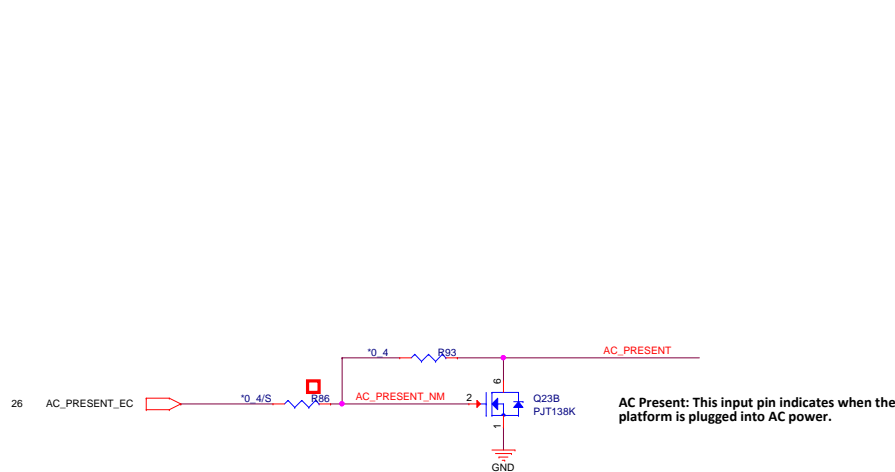
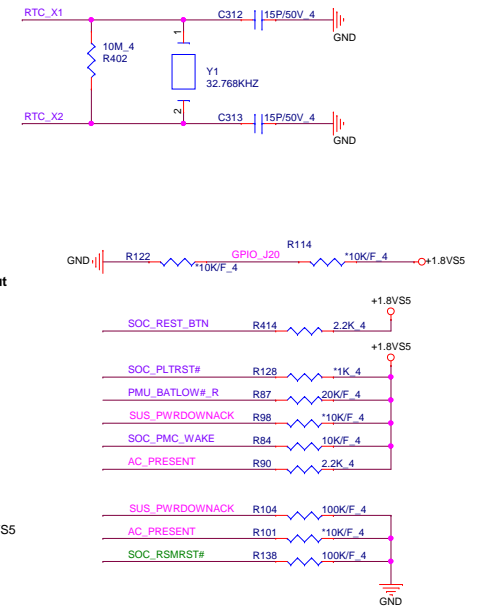
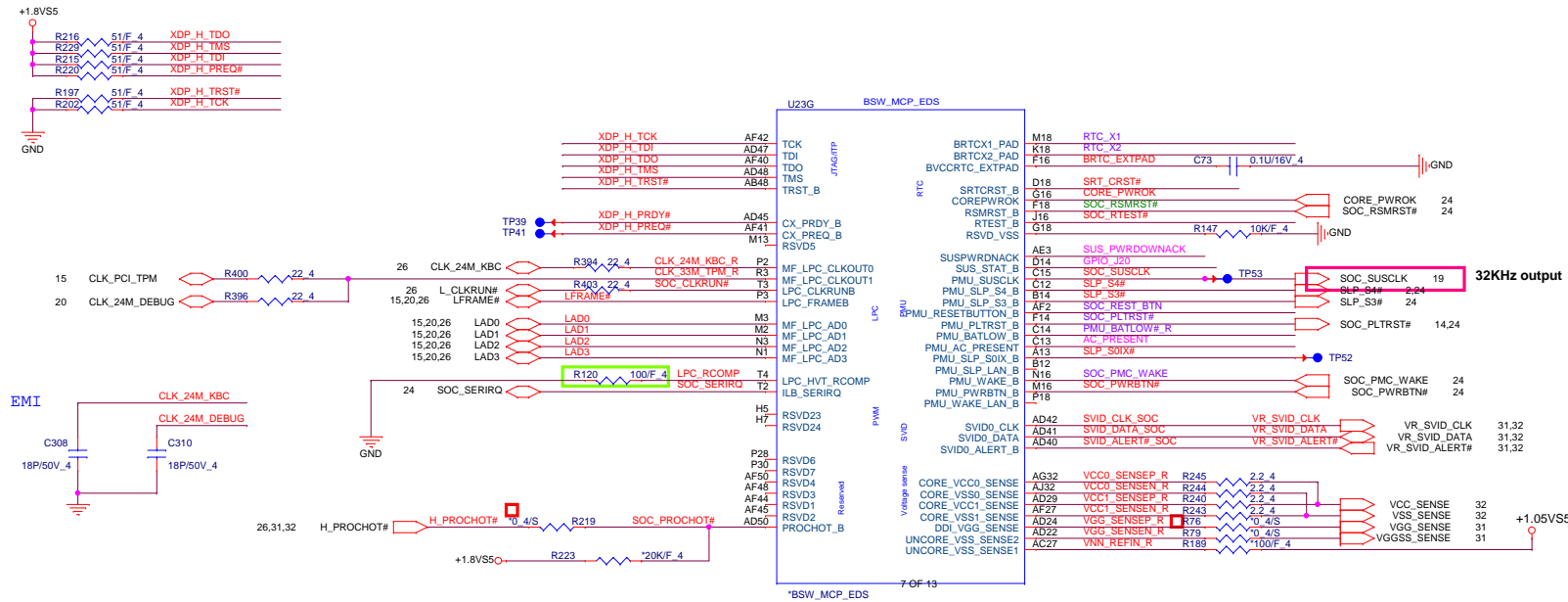
Need to discuss with BIOS



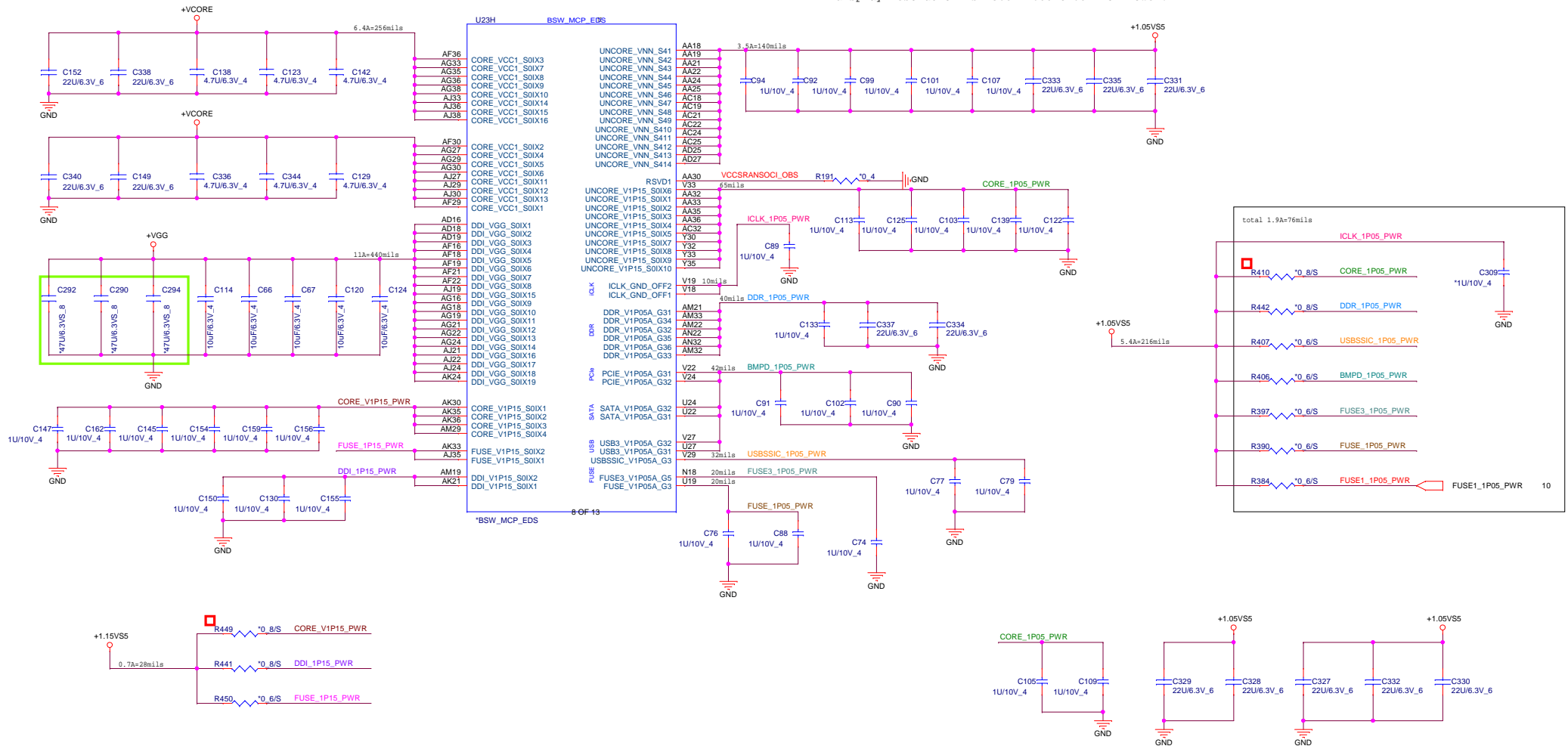
Quanta Computer Inc. PROJECT : OP9		Rev.
		1A
Size Custom	Document Number Braswell-5 (I2C/GPIO/CLK)	
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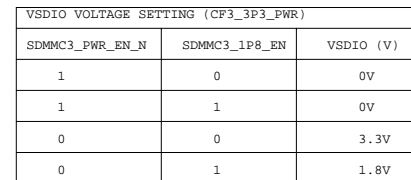


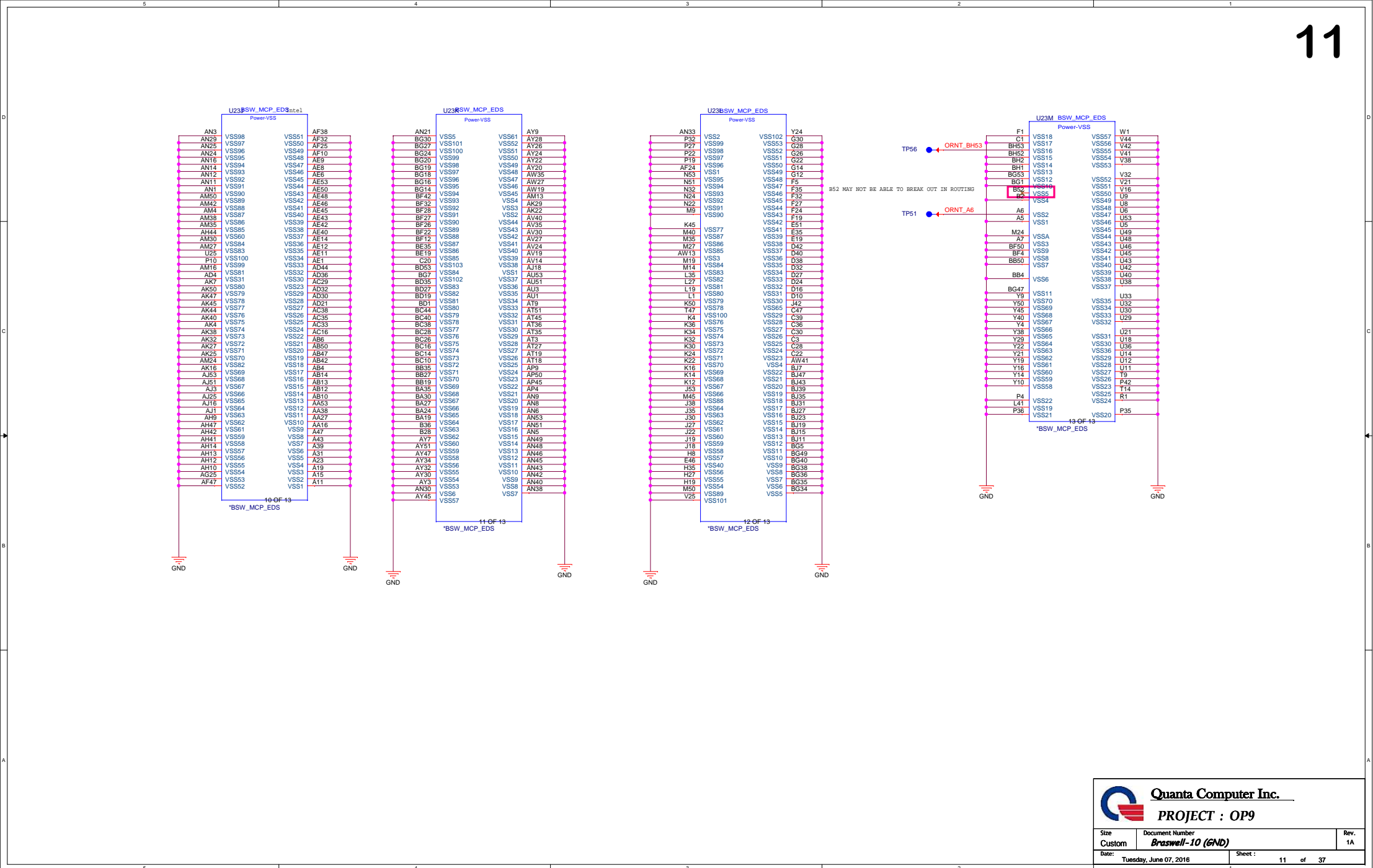
RTC Clock 32.768KHz

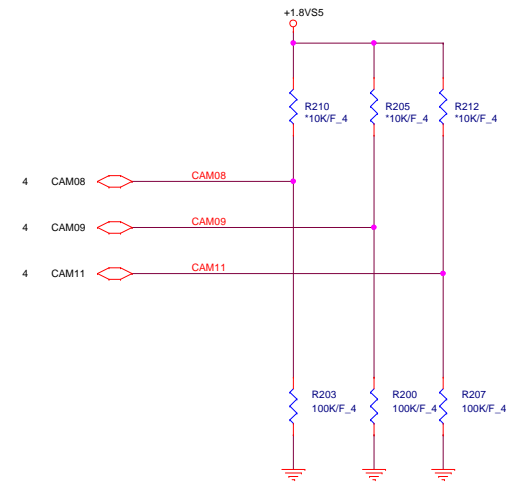


VNN can optionally be merged with V1P05A
if display resolution is 2560 x1600 @ 60Hz or lower.

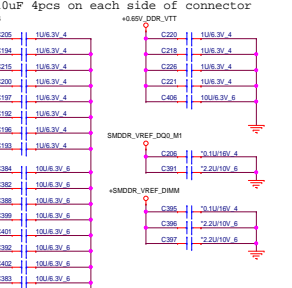
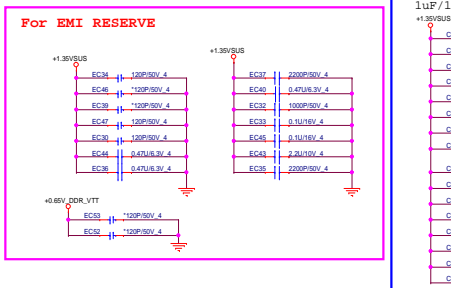
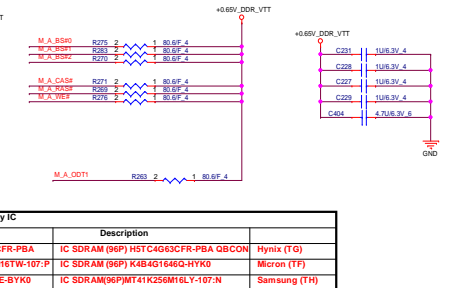
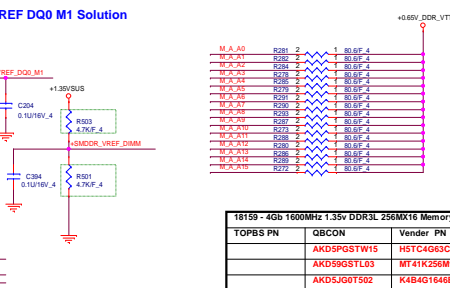
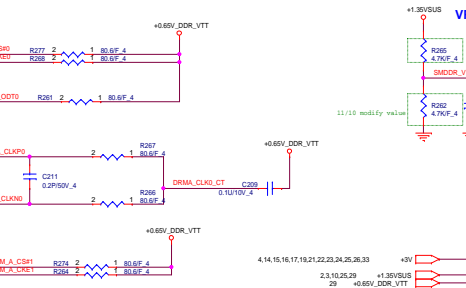
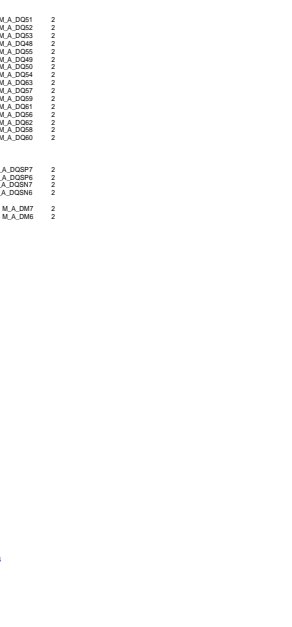
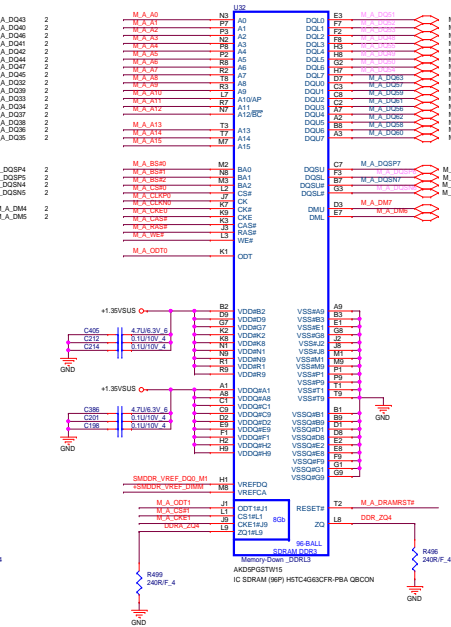
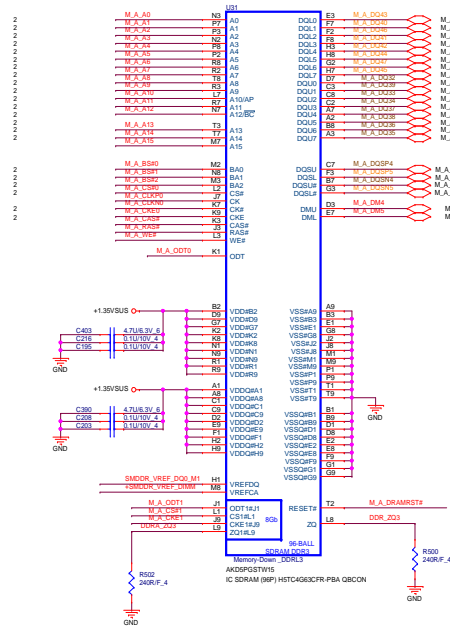
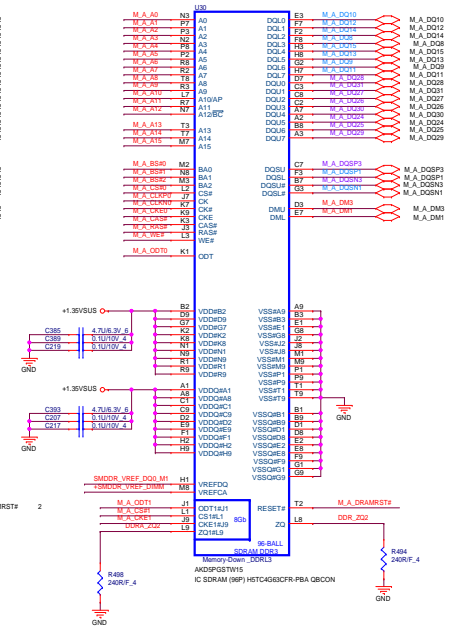
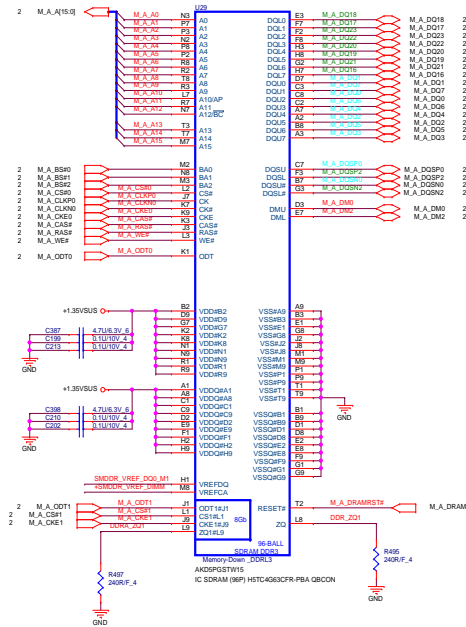




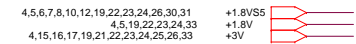









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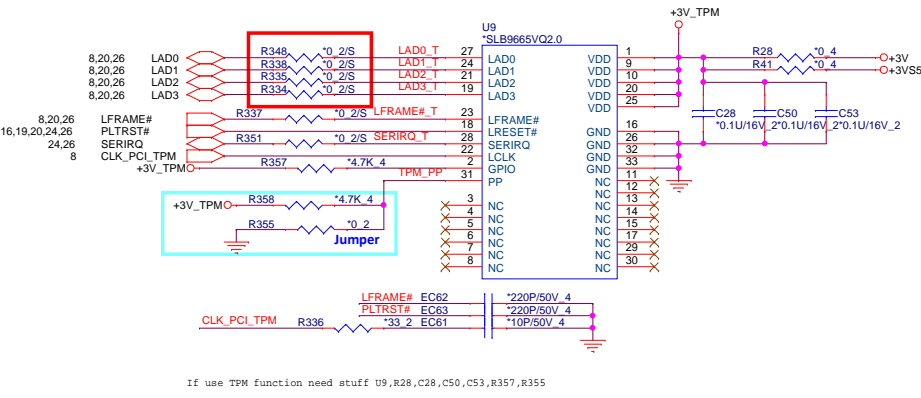


16159 - 4Gb 1600MHz 1.35v DDR3L 256MX16 Memory IC				
TOPBS PN	QBCON	Vendor PN	Description	
	AKDS9GTW15	H8Y4C6G3CFR-PBA	IC SDRAM (8P) H8Y4C6G3CFR-PBA QBCON	Hynix (TG)
	AKDS9GT03L03	MT41K256M16TW-107-P	IC SDRAM (8P) K4B4G1646E-HYK0	Micron (TF)
	AKDSJG01502	K4B4G1646E-BYK0	IC SDRAM(8P)MT41K256M16L-Y107-N	Samsung (TH)



Memory setting		Location				
Vender	SIZE	R151  ~"10K_4	R144  ~"10K_4	R143  ~"10K_4	R136  ~"10K_4	R135  ~"10K_4
Hynix	2G	1	0	0	0	0
samaung	2G	1		0	0	0
Micron	2G	1	1	1	0	0
Hynix	4G	1	1	1	1	0
samaung	4G	0	1	1	1	1
Micron	4G	0	0	1	1	1
samaung E	2G	0	0	0	1	1
Micron P	2G	0	0	0	0	1
		0	0	0	0	0
		1	1	1	1	1

TPM (2.0)



Accelerometer Sensor

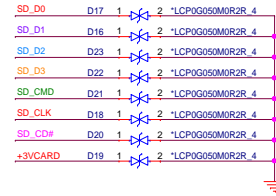
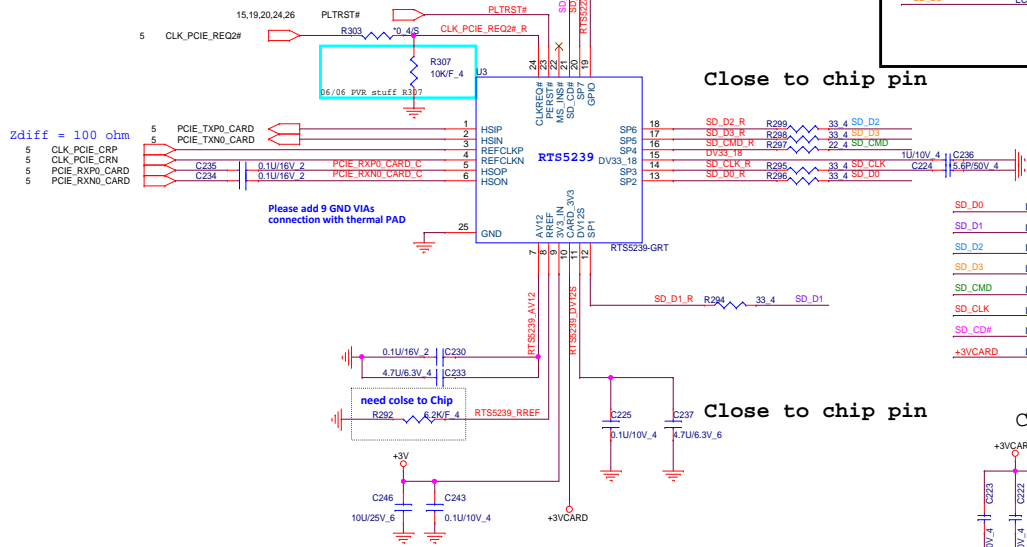
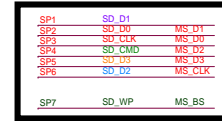
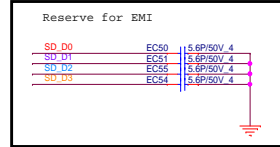
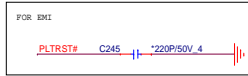


15

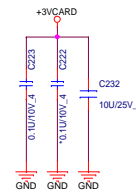
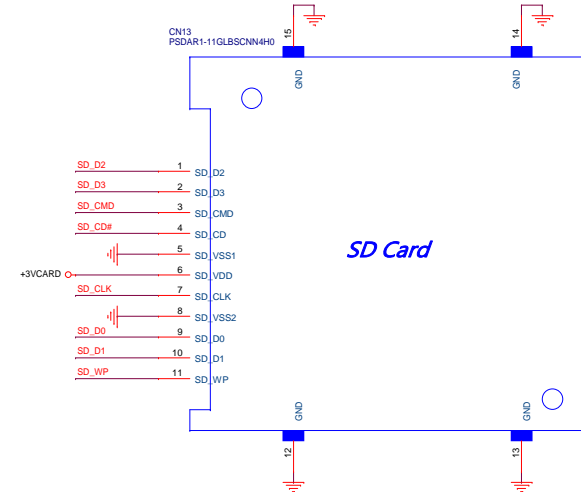
Touch screen

Green CLK Circuitry

CardReader



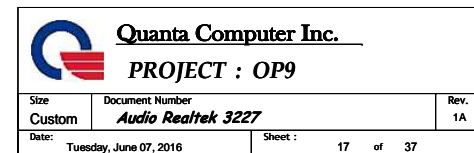
CLOSE CONN

SD CARD READER
(CD : Normal open)

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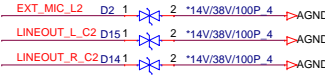
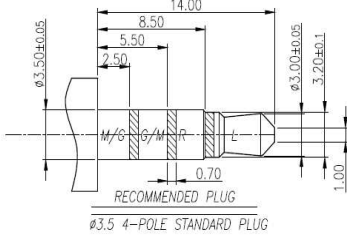
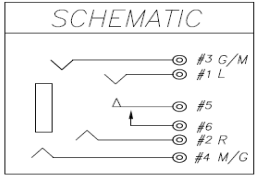
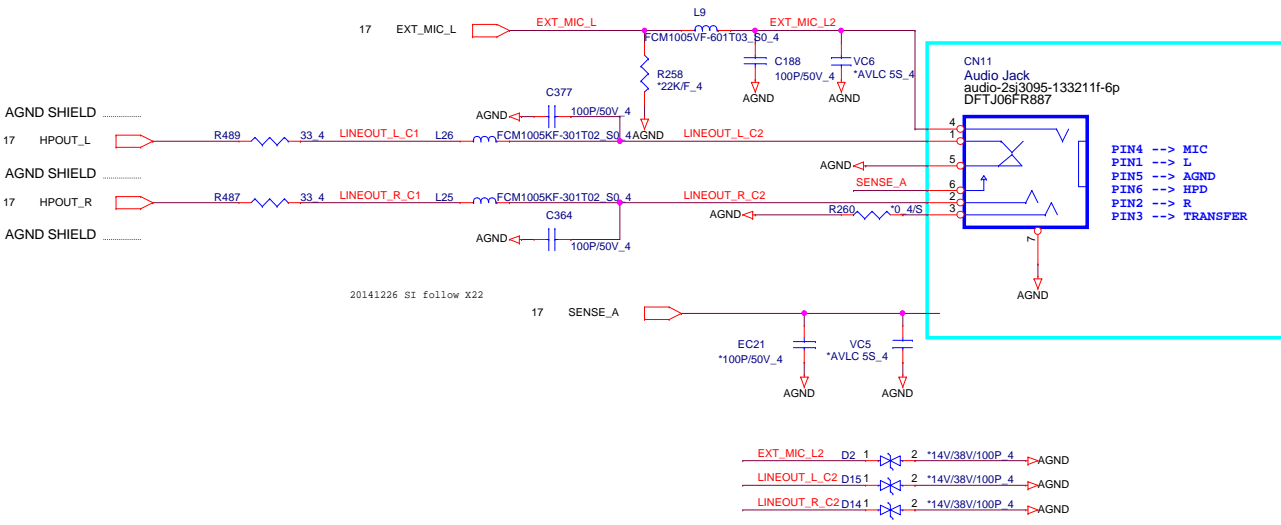
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Head Phone out

17,22,33 +5V
4,14,15,16,17,19,21,22,23,24,25,26,33 +3V

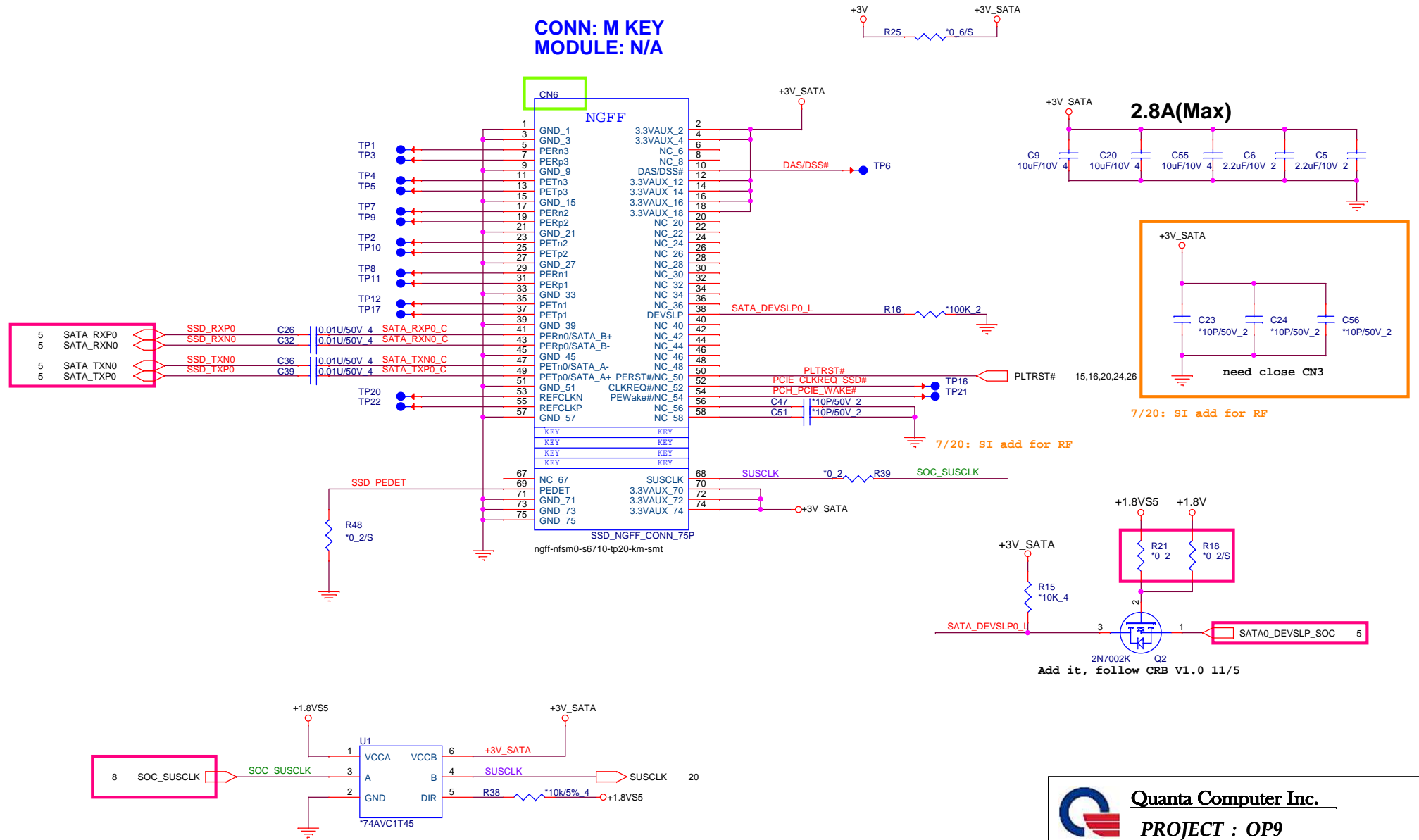


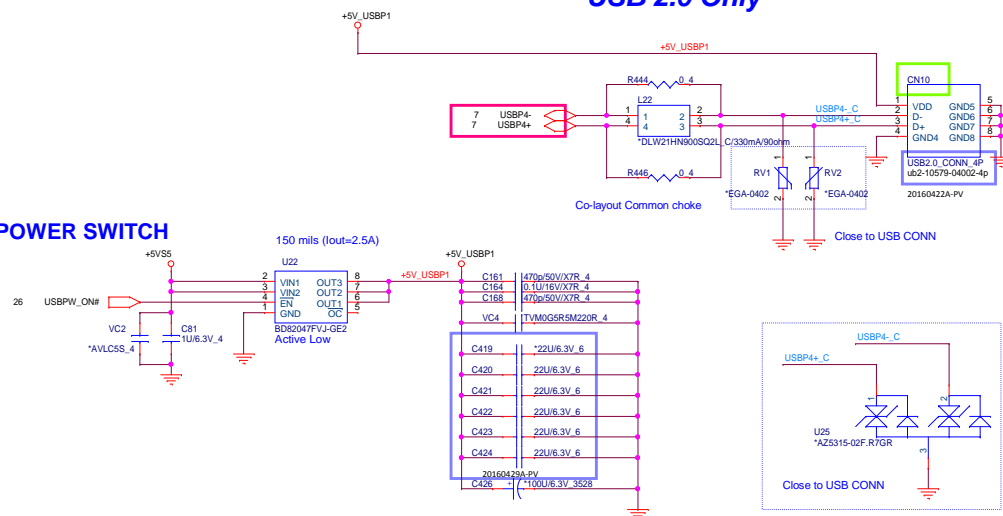
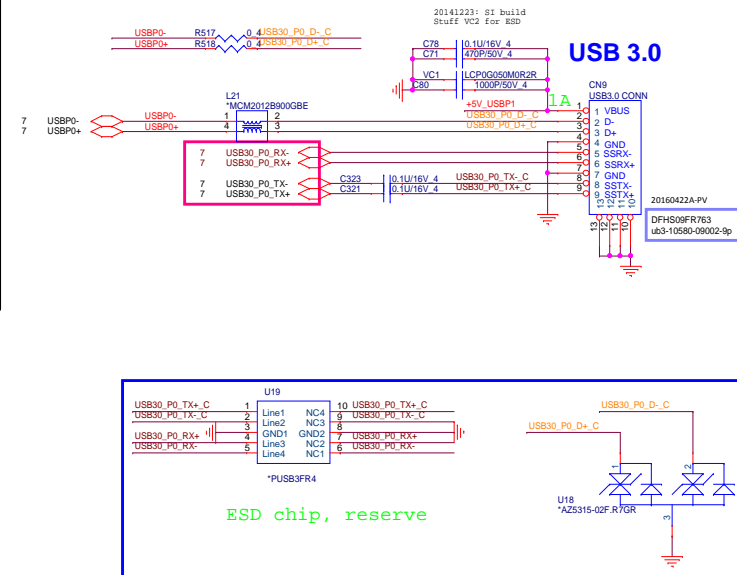
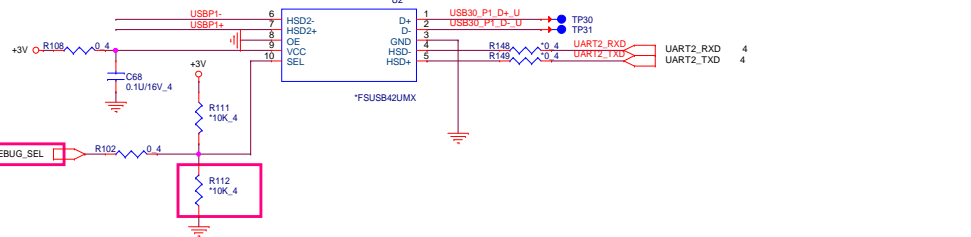
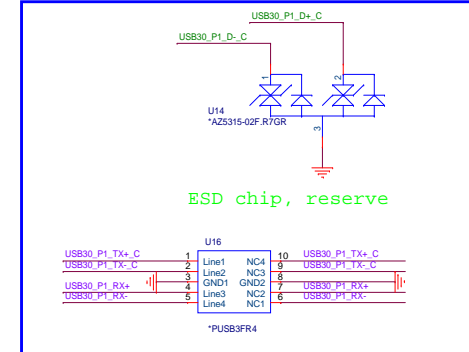


Quanta Computer Inc.
PROJECT : OP9

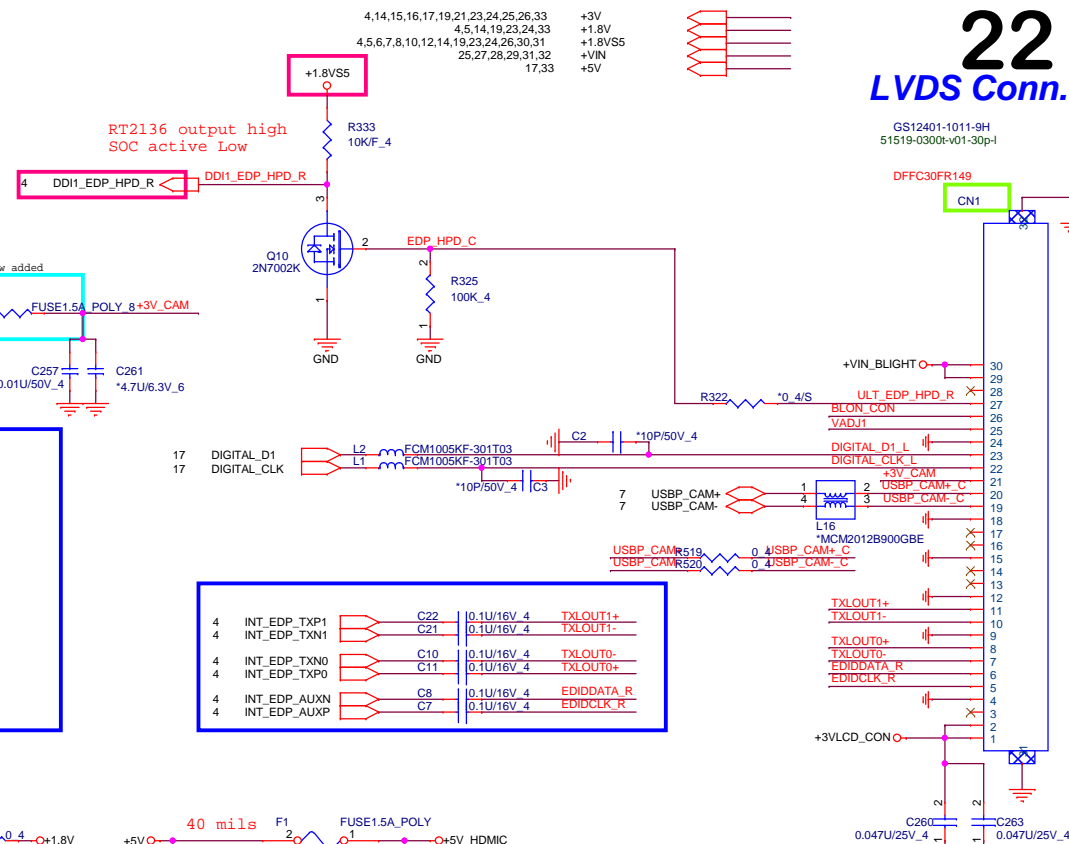
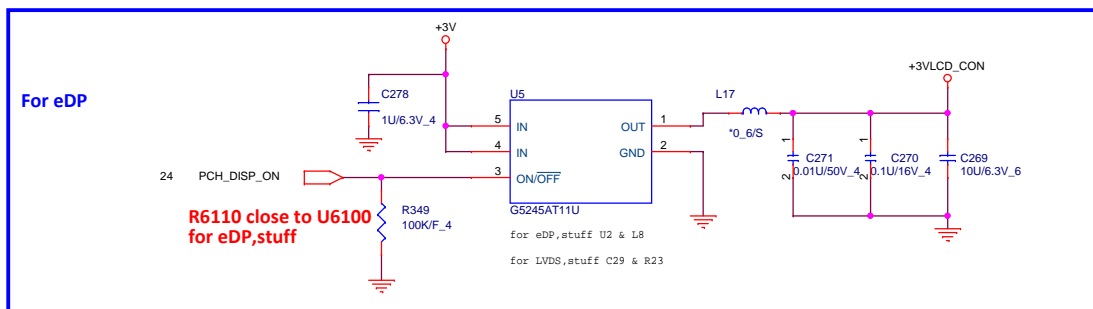
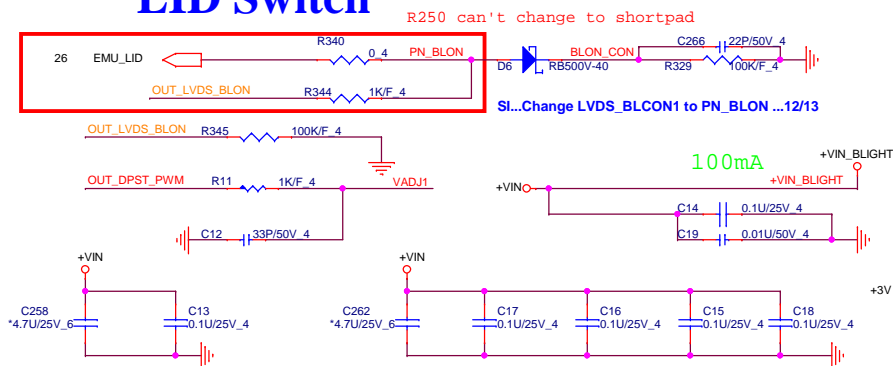
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CONN: M KEY MODULE: N/A

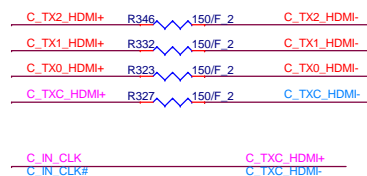




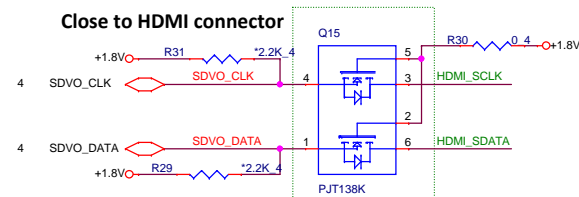
LID Switch



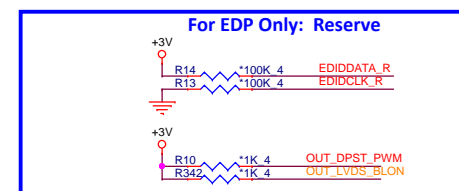
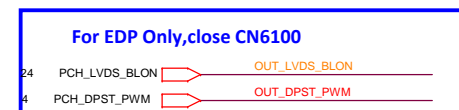
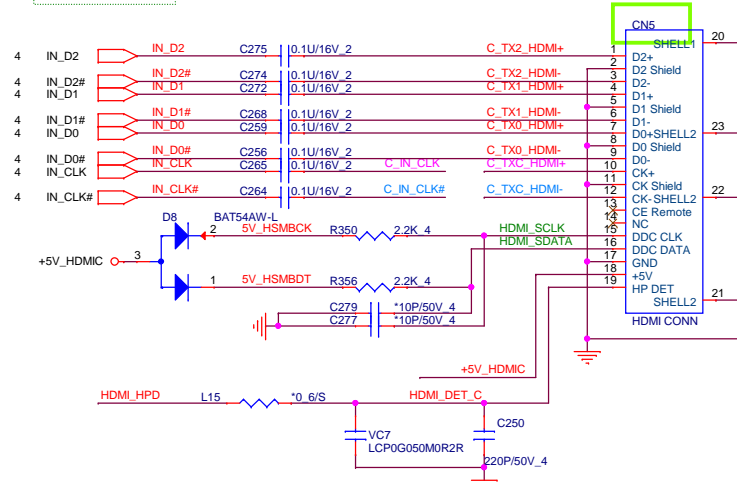
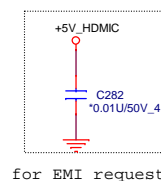
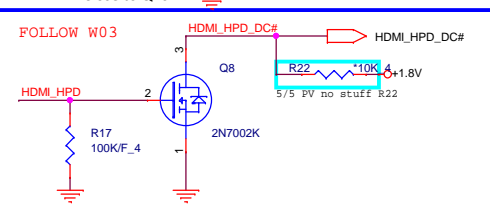
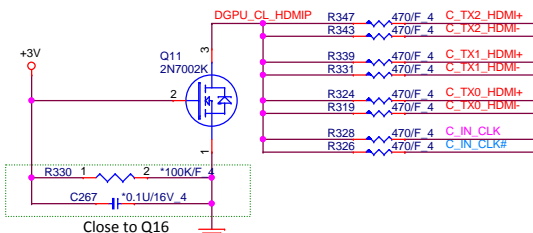
EMI Solution

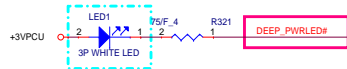


HDMI SMBus isolation



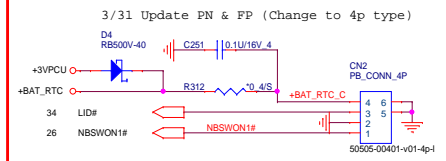
Close to HDMI connector



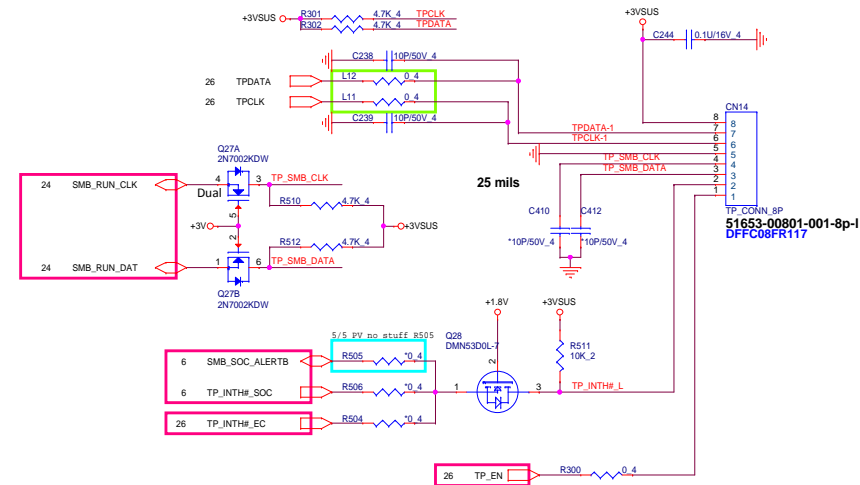


PN WAIT CONFIRM!

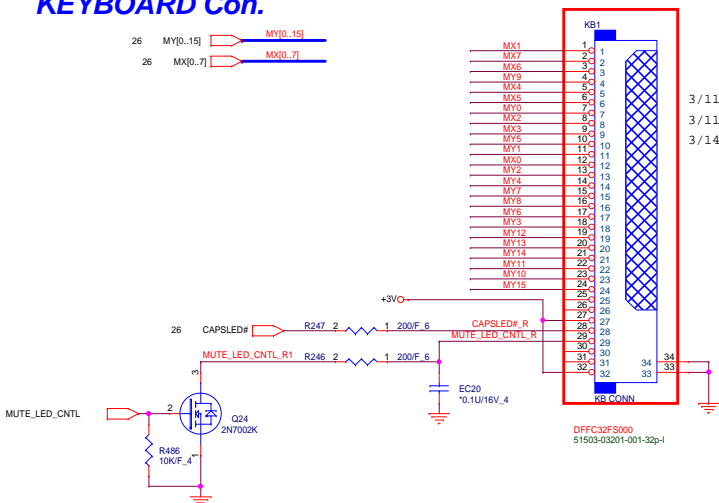
3/31 Update PN & FP (Change to 4p type)



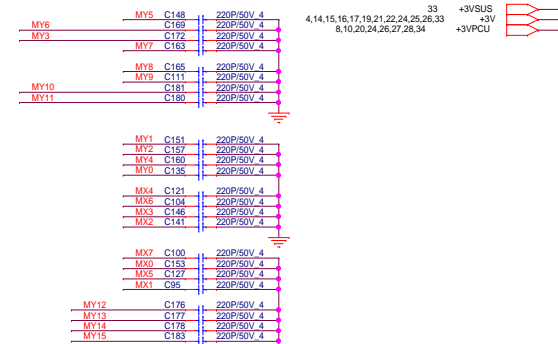
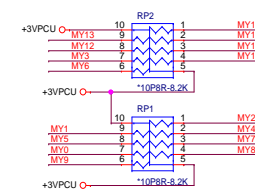
Touch Pad Connector



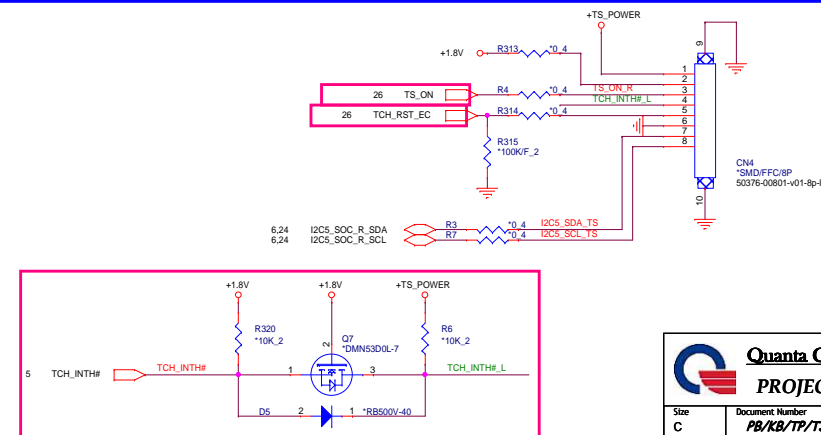
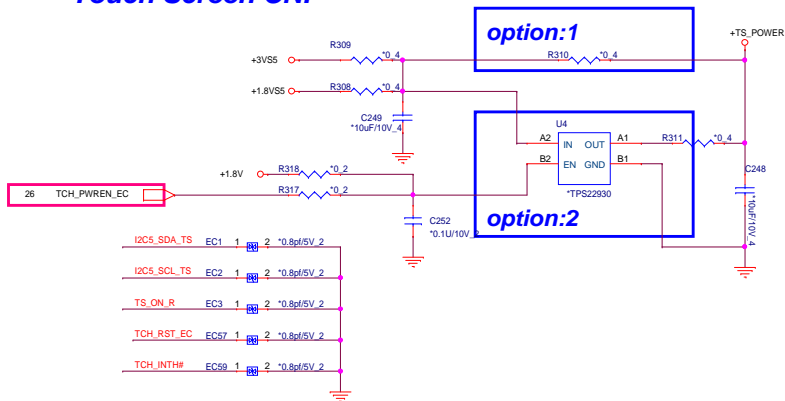
KEYBOARD Con.

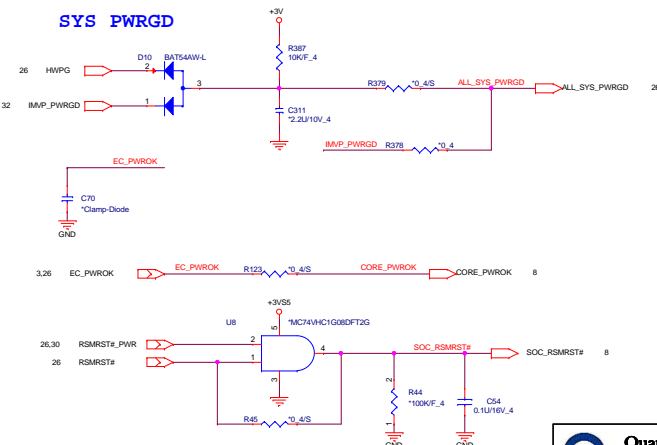
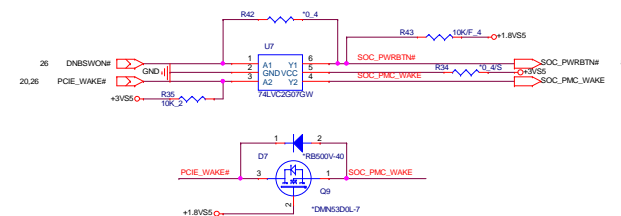
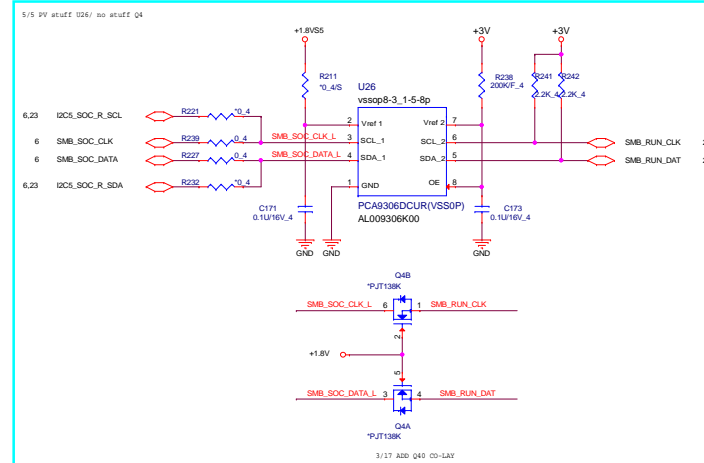
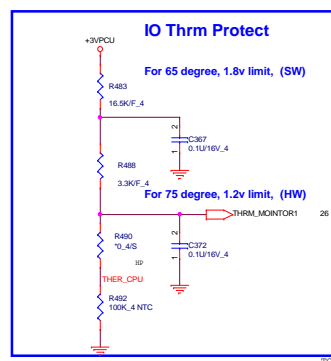
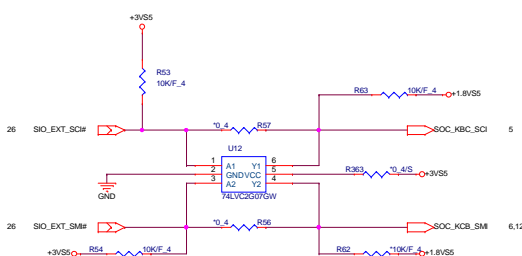
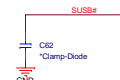
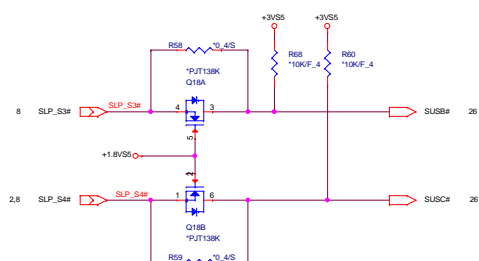
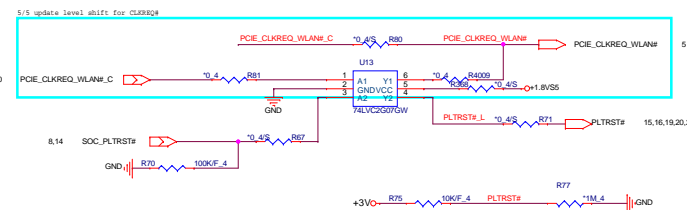
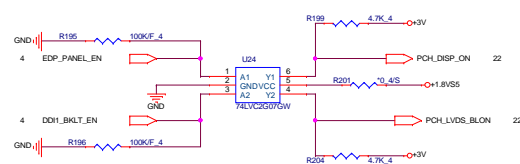
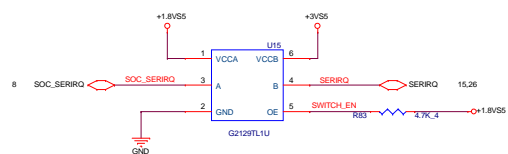


KEYBOARD PULL-UP

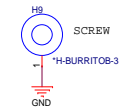
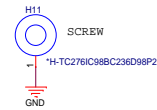
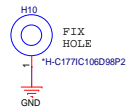
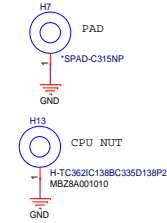
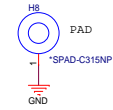
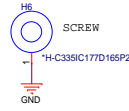
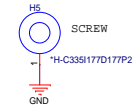
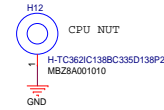
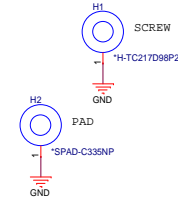
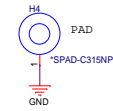
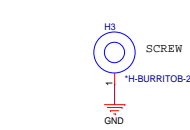
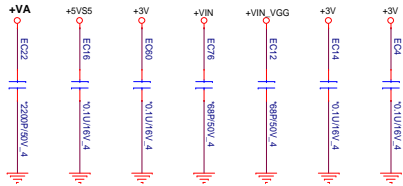
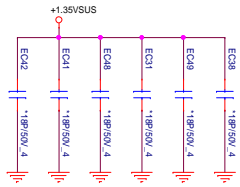
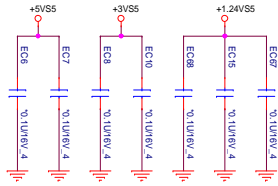
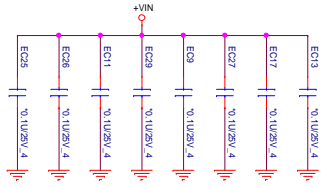


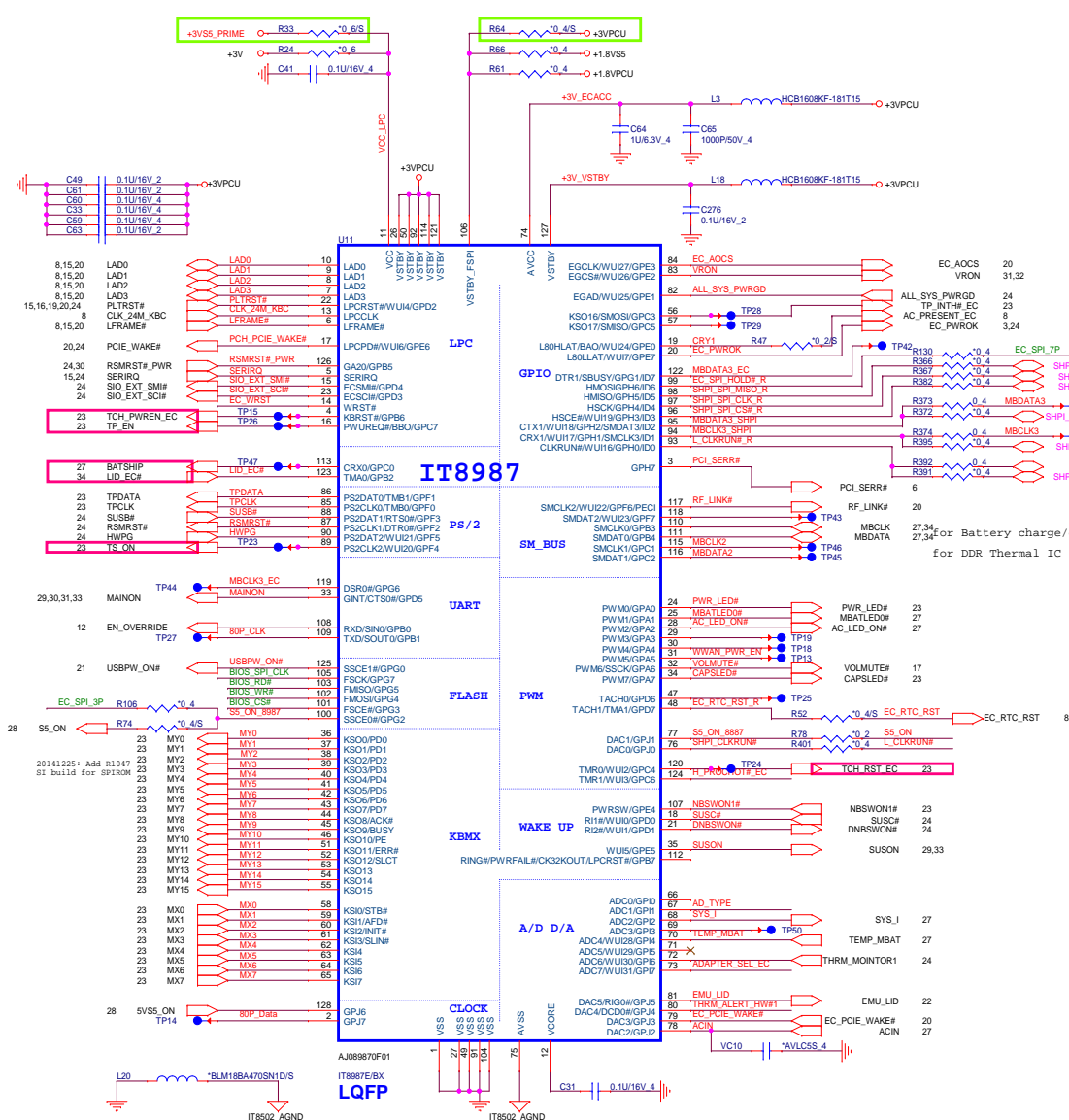
Touch Screen CN.





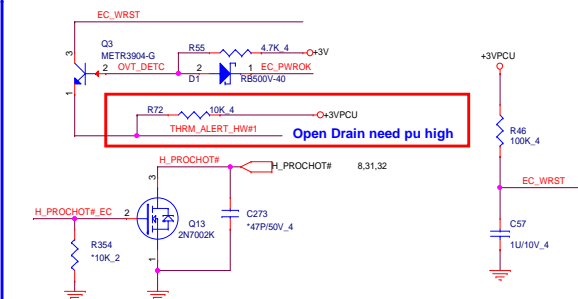
EMI Reserve





EC 8987 PN : AJ089870F01
EC 8887 PN : AJ088870T00
SI Build BOM Option

AKE5BY00000 / IC FLASH (8P) GD25D10BTIGR (SOP) / SPI,3V,1Mbit, SOP8 150mil.
AKE35ZN0N00 / IC FLASH(8P) W25X10CLSNIG(SOIC)/SPI, 2.3~3.6V, 1Mbit, 8-pinSOIC 150mil.

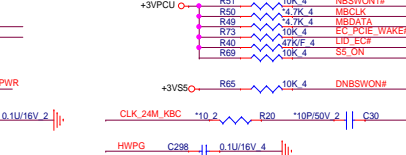
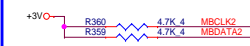
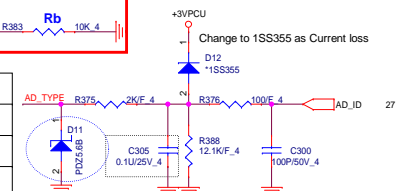


Adapter select for EC

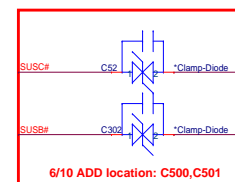


Adapter	90W	65W	45W
Ra	Stuff	Stuff	X
Rb	X	Stuff	Stuff

adapter Type check



Power Button Reset



4,14,15,16,17,19,21,22,23,24,25,33
8,10,20,23,24,27,28,34



+3VPCU 8,10,20,23,24,26,27,34
+5VPCU 27,30,33

+VIN 22,25,27,29,31,32
+3VS5 2,3,5,10,15,20,23,24,25,26,30,31,32,33
+5VS5 21,25,29,30,31,32,33
+1.8VPCU 26

+3.3 Volt +/- 5%
TDC:6A
EDP:8A

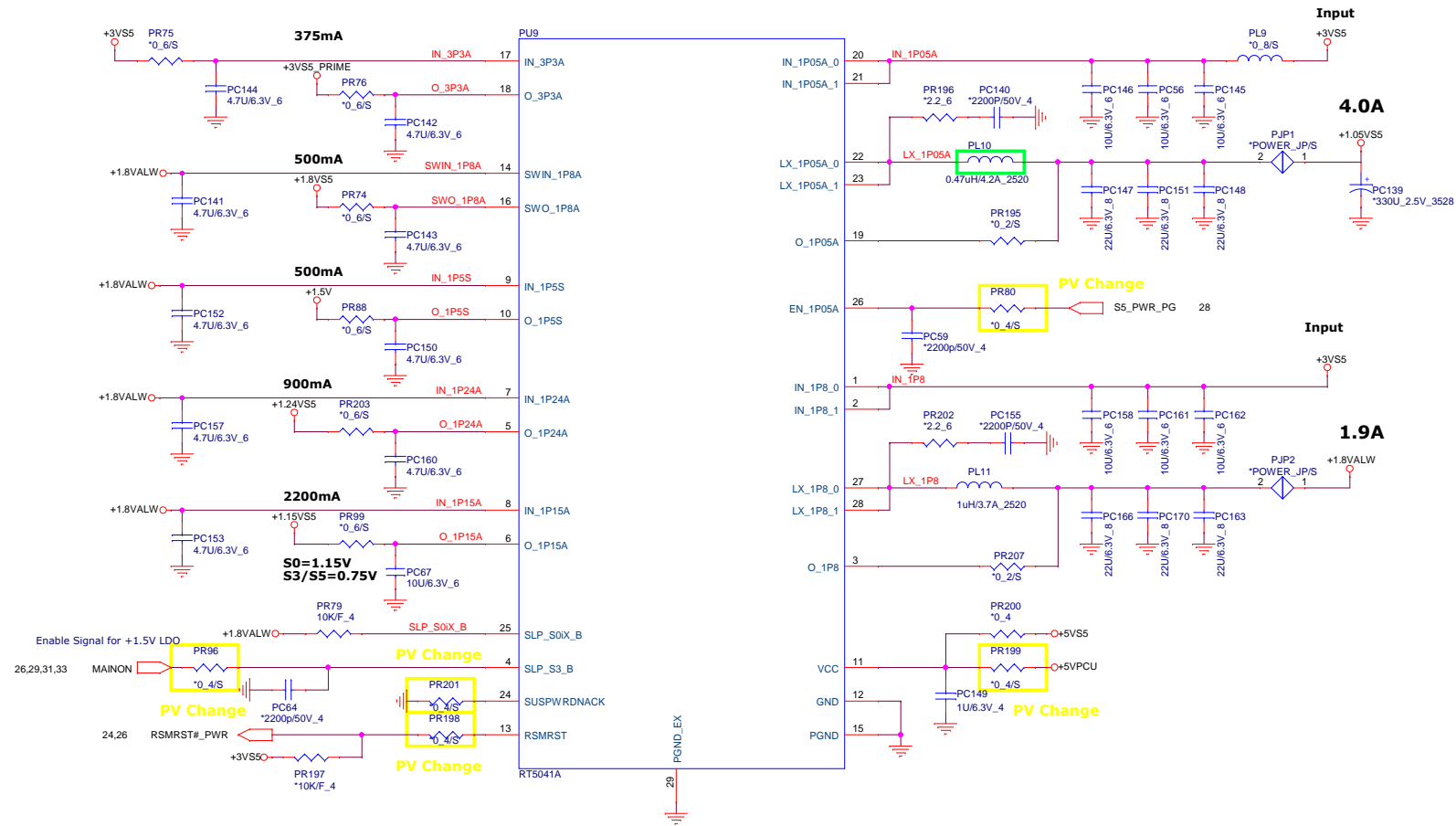
+5 Volt +/- 5%
TDC:6A
EDP:8A

USB Charge support	Ra	Rb
Vine (No support)	Stuff	NA
Envy (Support)	NA	Stuff

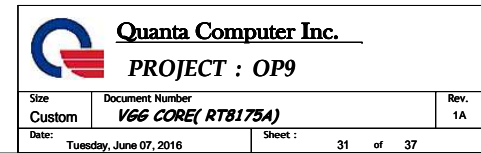
Do Not add test pad on VCC & LDO pin

Do Not add test pad on VCC & LDO pin

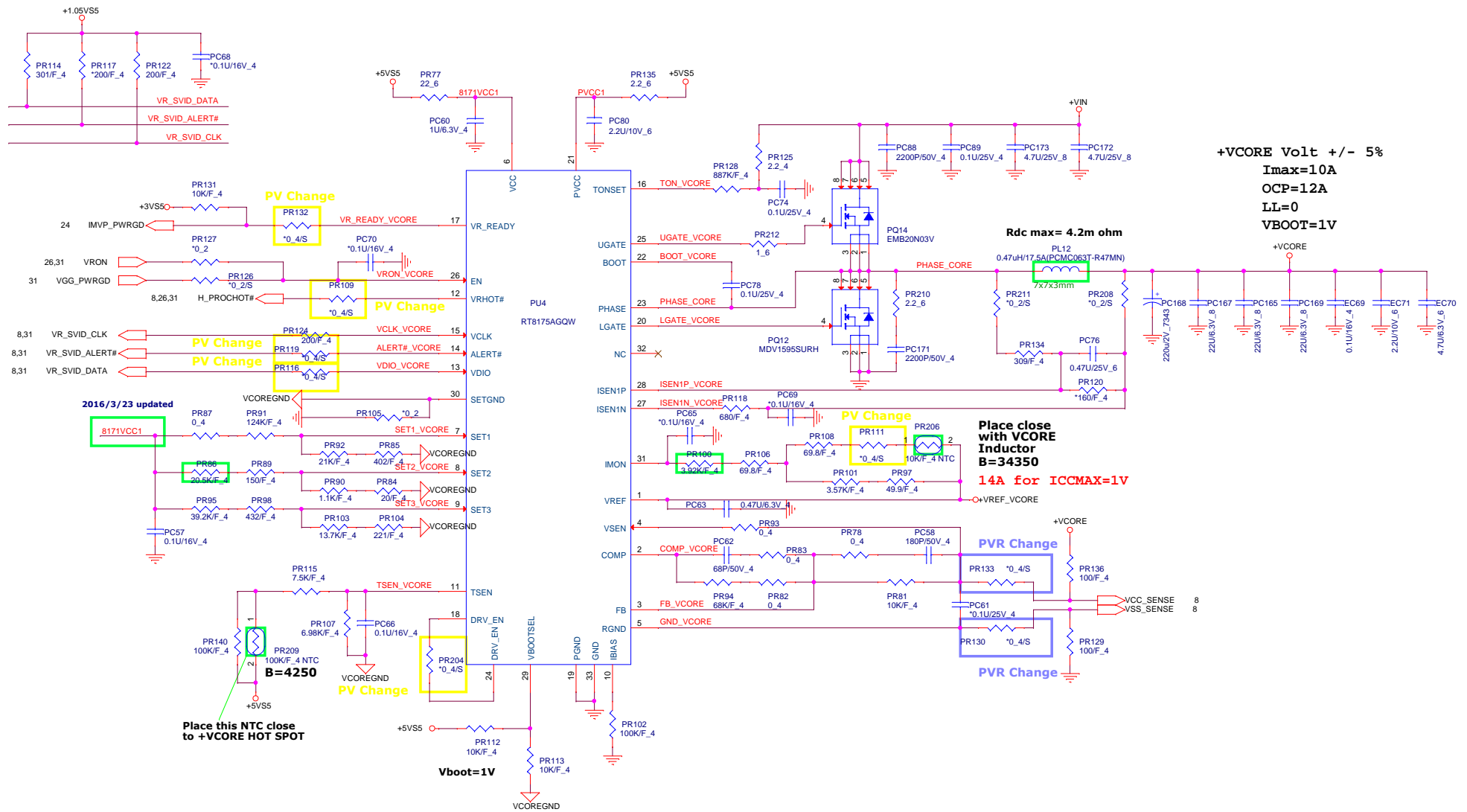




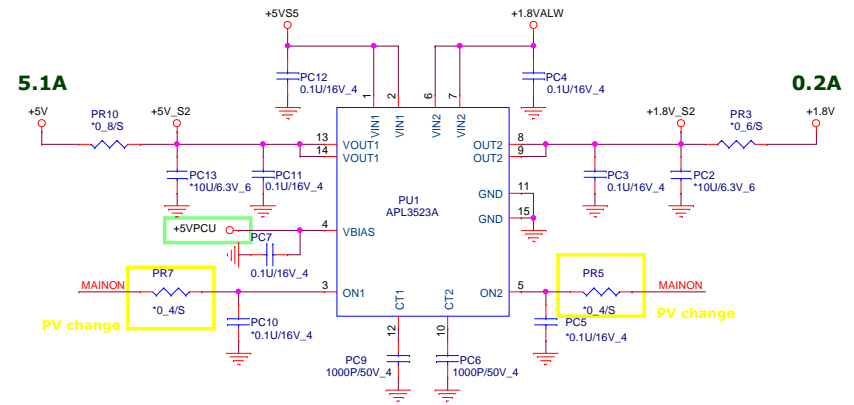
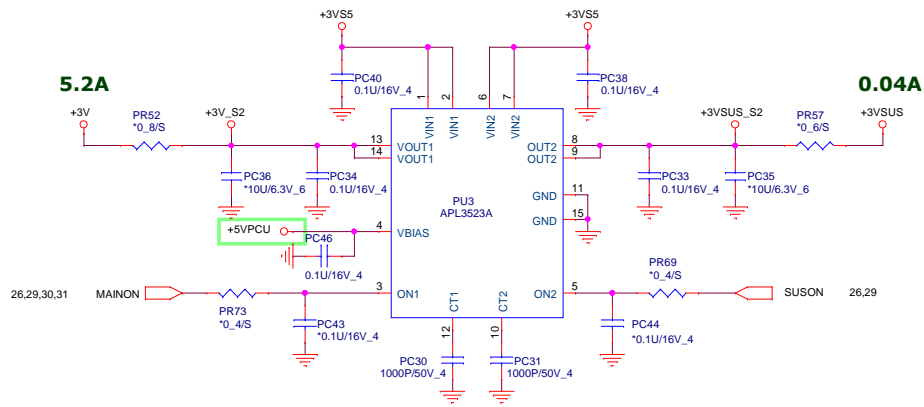
+3VS5	2,3,5,10,15,20,23,24,25,26,28,31,32,33
+1.8VALW	33
+1.8VS5	4,5,6,7,8,10,12,14,19,22,23,24,26,31
+3VS5_PRIME	10,26
+1.5V	10,17
+1.24VS5	10,25
+1.15VS5	9,30
+5VPCU	27,28,30,33
+1.05VS5	8,9,31,32
+1.15VS5	9,30
+5VPCU	27,28,30,33
+1.8V	4,5,14,19,22,23,24,33

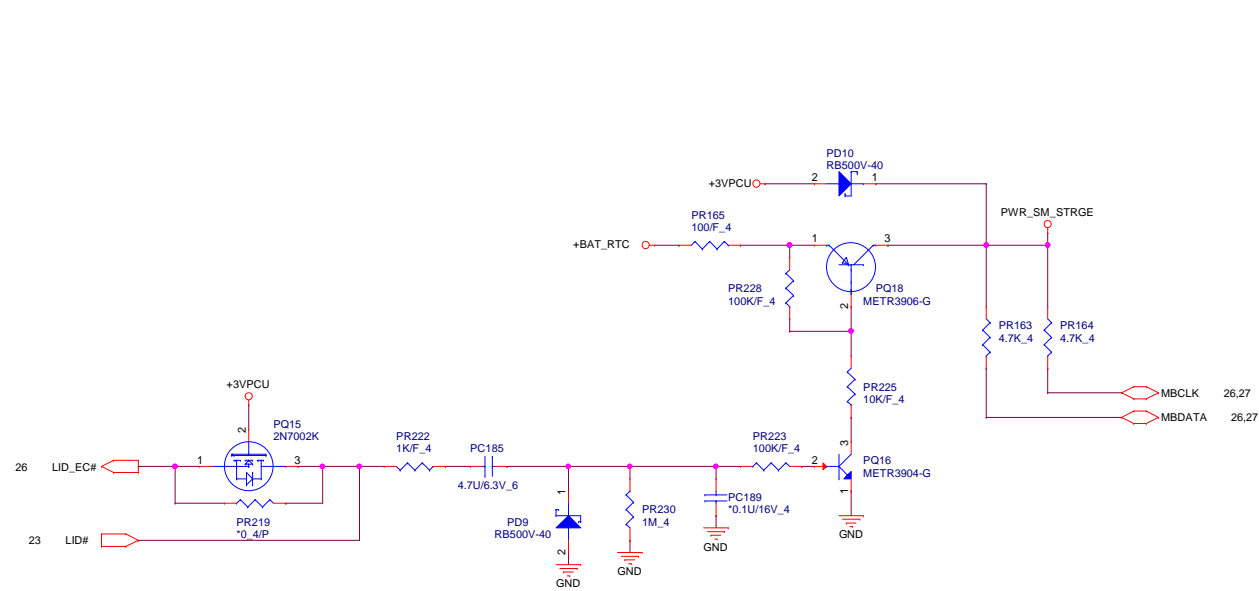


Close to CPU




+3V	4,14,15,16,17,19,21,22,23,24,25,26
+5V	17,22
+3VSUS	23
+5VPCU	27,28,30
+3VS5	2,3,5,10,15,20,23,24,25,26,28,30,31,32





8,10,20,23,24,26,27,28
23,27

+3VPCU
+BAT_RTC



```
graph LR
    A[+3VPCU] --> OR1[OR]
    B[+BAT_RTC] --> OR1
    OR1 --> C[23]
```

